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Notice of Allowability	Application No.	Applicant(s)	1
	10/721,492	TRESNESS ET AL.	
	Examiner	Art Unit	
	Dean O Takaoka	2817	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to <u>Applicant's RCE/IDS</u>	dated September 8, 2004.		
2. The allowed claim(s) is/are <u>1-7</u> .			
3. \boxtimes The drawings filed on <u>25 November 2003</u> are accepted by	the Examiner.		
4.			
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 9/8/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 08), 7. ☐ Examiner's Amend	ate	

Art Unit: 2817

DETAILED ACTION

Allowable Subject Matter

Claims 1 - 7 are allowed.

The following is an examiner's statement of reasons for allowance:

The prior art submitted in Applicant's IDS dated September 8, 2004 does not appear to show a ground plane disposed between the first and second circuit board surfaces. While the Applicant discusses Tratec Holland Low Pass Filter LPF-250 and High Pass Filter HPF 88 (Applicant's discussion of the prior art, see pages 1-3), it appears Tratec Holland shows a conventional well-known filter with components located on only one side of the circuit board and not both sides, nor where a ground plane is disposed between the first and second circuit board surfaces. Aarcom SAW negative Trap CATV Filter is discussed where components are located on the hidden side (page 2 with respect to Fig. 4) but does not appear to have a ground plane disposed between the first and second circuit board surfaces, thus the claims remain in condition for allowance.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Application/Control Number: 10/721,492

Art Unit: 2817

Page 3

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dean O Takaoka whose telephone number is (571) 272-

1772. The examiner can normally be reached on 8:30a - 5:00p Mon - Fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

dot November 2, 2004

Technology Center 2800